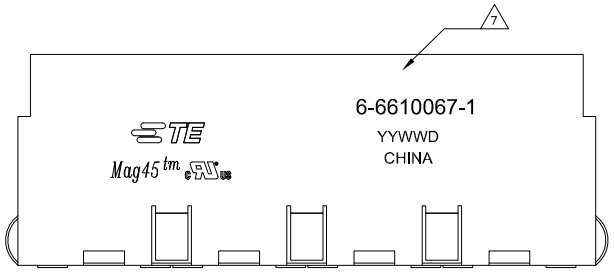
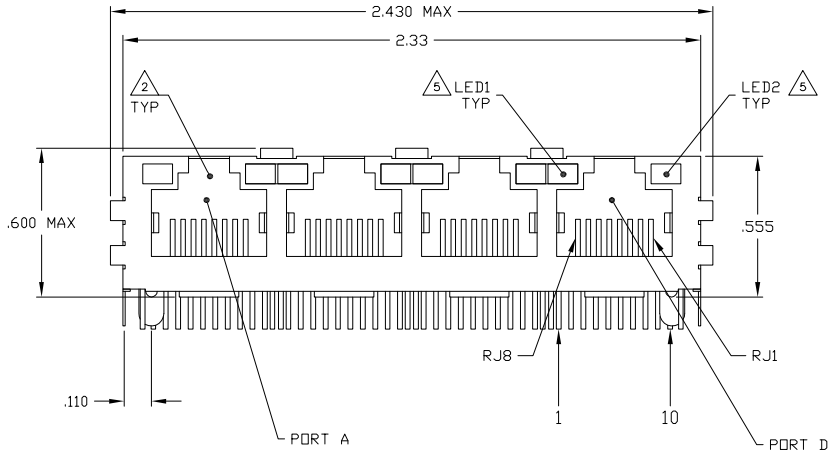
LOC	DIST
AA	22

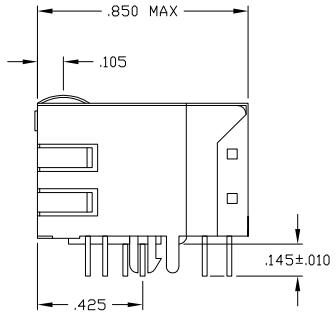
REVISIONS

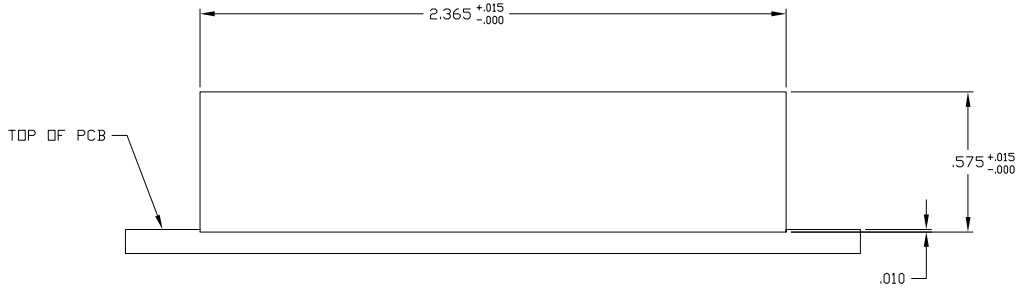
P	LTR	DESCRIPTION	DATE	DWN	APVD
	B	REV PER ECO-08-026409	02SEP2008	VL	TX
	C	ECO-11-012433	20MAY2011	EL	LR

MECHANICAL:









1X4 SUGGESTED PANEL CUTOUT

1. MATERIALS:
-HOUSING - THERMOPLASTIC PET POLYESTER FLAMMABILITY RATING UL 94V-0.
-SHIELD - .010" THICK, C26800 BRASS PREPLATED WITH 30µINCH MIN SEMI-BRIGHT NICKEL. SOLDER TABS POST DIPPED WITH 100µINCH MIN SAC SOLDER.
-MOD JACK CONTACTS - 0.0157 X 0.018" PHOSPHOR BRONZE, 50µINCH MIN OVERALL NICKEL UNDERPLATE WITH SELECT 50µINCH MIN HARD GOLD FINISH PLATE. SOLDERTAILS WITH 100µINCH MIN MATTE TIN AND/OR SAC SOLDER DIP.
-LIGHT EMITTING DIODE(LED) - DIFFUSED EPOXY LENS, .020" X .020" CARBON STEEL WIREFRAME LEADS PRE-PLATED WITH 80µINCH SILVER OVER 40µINCH NICKEL UNDERPLATE OVER 40µINCH COPPER UNDERPLATE. POST-PLATED WITH 100µIN MIN MATTE TIN AND/OR SAC SOLDER DIP OR PURE TIN SOLDER DIP.

2. RJ45 JACK CAVITY CONFORMS TO FCC RULES AND REGULATIONS PART 68, SUB PART F.

3. MAGNETICS
-IMPEDANCE: 100 OHMS
-TURNS RATIO (CHIP:CABLE): 1:1 ALL FOUR PAIRS
-OPEN CIRCUIT INDUCTANCE (OCL): 350uH MIN @100kHz, 0.1VRMS, 8mADC BIAS FROM 0°C TO 70°C, ALL FOUR PAIRS
-ALL FOUR PAIRS BI-DIRECTIONAL
-PERFORMANCE @ 25°C:
INSERTION LOSS (IL): 1.1dB MAX FROM 0.5MHz TO 100MHz
RETURN LOSS (RL): 18dB MIN FROM 0.5MHZ TO 40MHz
12-20LOG(f/80)dB MIN FROM 40.1MHz TO 100MHz
CROSSTALK ATTENUATION: 35dB MIN FROM 0.5MHz TO 40MHz
33-20*LOG(f/50)dB MIN FROM 40.1MHz TO 100MHz
COMMON MODE REJECTION RATIO (CMRR): 30dB MIN FROM 0.5MHz TO 100MHz
-ISOLATION VOLTAGE: 2250VDC (MAX) FOR 60 SECONDS WITH A RISE TIME OF 500V/SEC AND WITH ALL PORTS CONNECTED.

4. OPERATING TEMPERATURE: FROM 0°C TO +70°C.

5. IF THE LED WITH 250 OHM RESISTORS,LED IS DRIVEN WITH 5V VOLTAGE AND THE MAX OPERATING CURRENT IS 20mA.
LED COLOR : DOMINANT WAVELENGTH (λD): GREEN 568 nm TYP. @ VF=5V
FORWARD CURRENT (IF): GREEN 12 mA TYP. @ VF=5V

6. INDICATED MAGNETIC CONNECTIONS ARE SYMMETRICAL TO SUPPORT AUTO-MDI/MDIX.

7. TE CONNECTIVITY LOGO, PART NUMBER, DATE CODE, COUNTRY OF ORIGIN AND AGENCY APPROVAL MARKING IN APPROXIMATE LOCATION SHOWN.

8. THE PART IS RECOMMENDED FOR WAVE SOLDERING PROCESS,PREHEAT TEMPERATURE IS 120°C TO 160°C, 120 SECONDS TO 180 SECONDS, PEAK WAVE SOLDERING TEMPERATURE IS 260°C MAX,10 SECONDS MAX.

GREEN	GREEN	6-6610067-1
LED1 5	LED2 5	PART NUMBER

THIS DRAWING IS A CONTROLLED DOCUMENT.

DIMENSIONS: INCHES

TOLERANCES UNLESS OTHERWISE SPECIFIED:
0 PLC ± -
1 PLC ± -
2 PLC ± .010
3 PLC ± .005
4 PLC ± -
ANGLES ± -

MATERIAL 1
FINISH 1

DWN 17MAR2005 L. VARELA - DOCK5
CHK 17MAR2005 D. FAROLE
APVD 17MAR2005 D. FAROLE
PRODUCT SPEC 108-2100
APPLICATION SPEC —
WEIGHT —

TE Connectivity

NAME 1X4 MAG45(TM) MODULAR JACK, 7G1P1 SCHEMATIC, (10 PIN HORIZONTAL), 7G01P1 MAGNETIC CIRCUIT, DECOUPLING CAPACITOR, WITH RESISTOR LEDS

SIZE	CAGE CODE	DRAWING NO	RESTRICTED TO
A1	00779	C-6610067	—

CUSTOMER DRAWING | SCALE 4:1 | SHEET 1 OF 2 | REV C

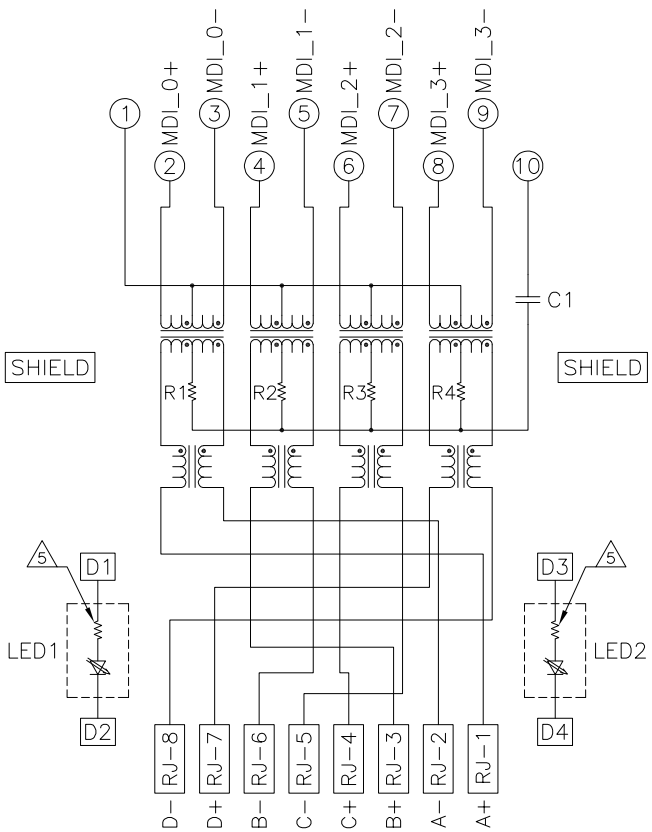
LOC	DIST
AA	22

REVISIONS

P	LTR	DESCRIPTION	DATE	DWN	APVD
	—	SEE SHEET 1	—	—	—

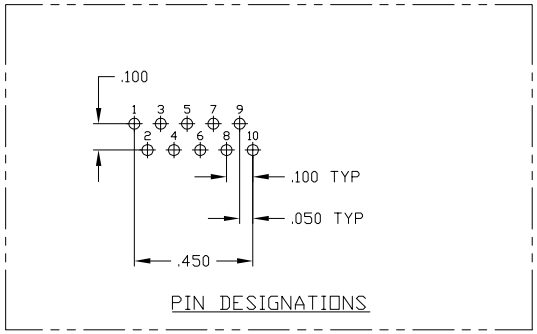
ELECTRICAL:

7G01P1 MAGNETIC CIRCUIT 3 6

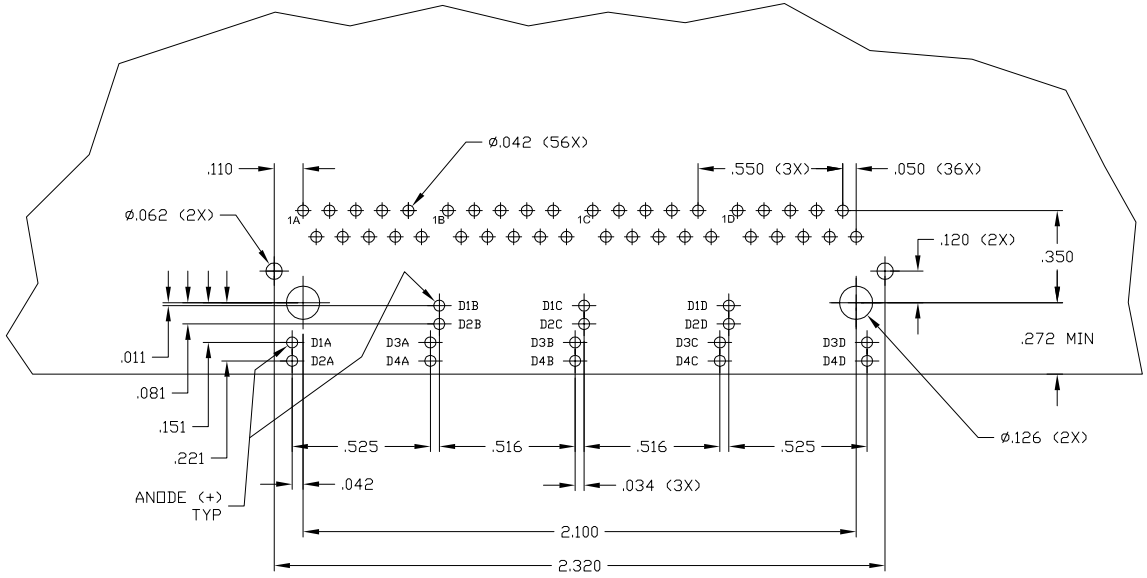


C1 = 1000 pF, 2kV CAPACITOR

R1–R4 = 75 OHMS, 1/16W RESISTORS



PIN DESIGNATIONS



SUGGESTED PCB LAYOUT
(Component Side)

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN L. VARELA – DOCK5 17MAR2005	TE Connectivity
DIMENSIONS: INCHES	TOLERANCES UNLESS OTHERWISE SPECIFIED: 0 PLC ± – 1 PLC ± – 2 PLC ± .010 3 PLC ± .005 4 PLC ± – ANGLES ± –	CHK D. FAROLE 17MAR2005	
		APVD D. FAROLE 17MAR2005	NAME 1X4 MAG45(TM) MODULAR JACK, 7G1P1 SCHEMATIC, (10 PIN HORIZONTAL), 7G01P1 MAGNETIC CIRCUIT, DECOUPLING CAPACITOR, WITH RESISTOR LEDS
MATERIAL –	FINISH –	PRODUCT SPEC 108–2100	SIZE A1 / CAGE CODE 00779 / DRAWING NO C-6610067 / RESTRICTED TO –
		APPLICATION SPEC –	
		WEIGHT –	CUSTOMER DRAWING / SCALE 4:1 / SHEET 2 OF 2 / REV C